

PATENT
Docket No. 188122000400**DECLARATION FOR UTILITY PATENT APPLICATION****AS BELOW-NAMED INVENTORS, WE HEREBY DECLARE THAT:**

Our residences, post office addresses, and citizenship are as stated below next to our names.

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled: **ROBUST CALCULATION OF CROSSTALK DELAY CHANGE IN INTEGRATED CIRCUIT DESIGN**, the specification of which is attached hereto unless the following box is checked:

☒ was filed on December 12, 2003, as United States Application Serial No. 10/735,123.

WE HEREBY STATE THAT WE HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE.

We acknowledge the duty to disclose information which is material to the patentability as defined in 37 C.F.R. § 1.56.

We hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Application No.	Country	Date of Filing	Priority Claimed?
			<input type="checkbox"/> Yes <input type="checkbox"/> No

We hereby claim benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

Application Serial No.	Filing Date
60/504,127	September 19, 2003

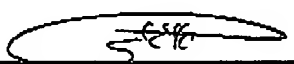
We hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, we acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

Application Serial No.	Filing Date	Status
		<input type="checkbox"/> Patented <input type="checkbox"/> Pending <input type="checkbox"/> Abandoned

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

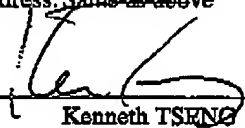
05/19/04

Date


Name: Igor KELLER
Residence: 446 Peralta Avenue, Sunnyvale, California 94086
Citizenship: Israel
Post Office Address: Same as above


5/19/04

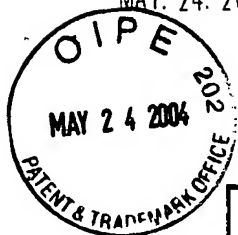
Date


Name: Kenneth TSENG
Residence: 7632 Erin Way, Cupertino, California 95014
Citizenship: United States
Post Office Address: Same as above

05/19/04

Date


Name: Nishath VERGHESE
Residence: 426 Ives Terrace, Sunnyvale, California 94087
Citizenship: India
Post Office Address: Same as above



PTO/SB/81 (08-03)

Approved for use through 11/30/2005. OMB 0651-0036
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

POWER OF ATTORNEY and CORRESPONDENCE ADDRESS INDICATION FORM	Application Number	10/735,123
	Filing Date	December 12, 2003
	First Named Inventor	Igor KELLER
	Title	ROBUST CALCULATION OF CROSSTALK DELAY CHANGE IN INTEGRATED CIRCUIT DESIGN
	Art Unit	2816
	Examiner Name	Not Yet Assigned
Attorney Docket No.		188122000400

I hereby appoint:

☒ Practitioners at Customer Number 20872

OR

☐ Practitioner(s) named below:

Name	Registration Number	Name	Registration Number

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Please recognize or change the correspondence address for the above-identified application to:

☒ The above-mentioned Customer Number.

OR

☐ The address associated with
Customer Number:

OR

☐ Firm or
Individual Name

Address

City

State

Zip

Country

Telephone

Fax

I am the:

☐ Applicant/Inventor.

☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/98).

SIGNATURE of Applicant or Assignee of Record

Name	Michael J. Williams, VP & Associate General Counsel		
Signature	<i>Michael J. Williams</i>		
Date	5-21-04	Telephone	408-894-2430

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☐ *Total of 1 forms are submitted.



PTO/SB/98 (08-03)

Approved for use through 04/30/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Igor KELLER et al.Application No./Patent No.: 10/735,123Filed/Issue Date: December 12, 2003Entitled: ROBUST CALCULATION OF CROSSTALK DELAY CHANGE IN INTEGRATED CIRCUIT DESIGNCadence Design Systems, Inc.

, a

corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.

The extent (by percentage) of its ownership interest is _____ %

In the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
2. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

[] Additional documents in the chain of title are listed on a supplemental sheet.

- ☒ Copies of assignments or other documents in the chain of title are attached.
[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

5-21-04

Date

408-894-2430

Telephone Number

Cadence Design Systems, Inc.

Typed or printed name

Michael J. Williams

Signature

VP and Associate General Counsel

Title

Attorney Docket No. 188122000400

sf-1656211

**COPY**

Atty Docket 188122000400

**ASSIGNMENT
JOINT**

THIS ASSIGNMENT, by Igor KELLER, Kenneth TSENG and Nishath VERGHESE (hereinafter referred to as the assignors), residing at 446 Peralta Avenue, Sunnyvale, California 94086; 7632 Erin Way, Cupertino, California 95014 and 426 Ives Terrace, Sunnyvale, California 94087, respectively, witnesseth:

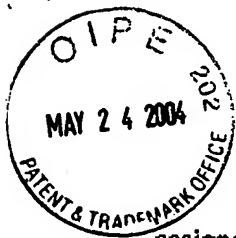
WHEREAS, said assignors have invented certain new and useful improvements in ROBUST CALCULATION OF CROSSTALK DELAY CHANGE IN INTEGRATED CIRCUIT DESIGN, set forth in an application for Letters Patent of the United States, bearing Serial No. 10/735,123 and filed on December 12, 2003; and

WHEREAS, Cadence Design Systems, Inc., a corporation duly organized under and pursuant to the laws of Delaware and having its principal place of business at 2655 Seely Avenue, Building 5, San Jose, California 95134 (hereinafter referred to as the assignee Cadence Design Systems, Inc.) is desirous of acquiring the entire right, title and interest in and to said inventions and said application for Letters Patent of the United States, and in and to any Letters Patent or Patents, United States or foreign, to be obtained therefor and thereon:

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and sufficient consideration, the receipt of which is hereby acknowledged, said assignors have sold, assigned, transferred and set over, and by these presents do sell, assign, transfer and set over, unto said assignee Cadence Design Systems, Inc., its successors, legal representatives and assigns, the entire right, title and interest in and to the above-mentioned inventions, application for Letters Patent, and any and all Letters Patent or Patents in the United States of America and all foreign countries which may be granted therefor and thereon, and in and to any and all divisions, continuations and continuations-in-part of said application, or reissues or extensions of said Letters Patent or Patents, and all rights under the International Convention for the Protection of Industrial Property, the same to be held and enjoyed by said assignee Cadence Design Systems, Inc., for its own use and the use of its successors, legal representatives and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted, as fully and entirely as the same would have been held and enjoyed by the assignors, had this sale and assignment not been made.

AND for the same consideration, said assignors hereby covenant and agree to and with said assignee Cadence Design Systems, Inc. its successors, legal representatives and assigns, that, at the time of execution and delivery of these presents, said assignors are the sole and lawful owners of the entire right, title and interest in and to said inventions and the application for Letters Patent above-mentioned, and that the same are unencumbered and that said assignors have good and full right and lawful authority to sell and convey the same in the manner herein set forth.

AND for the same consideration, said assignors hereby covenant and agree to and with said assignee Cadence Design Systems, Inc., its successors, legal representatives and assigns, that said



Atty Docket 188122000400

assignors will, whenever counsel of said assignee, or the counsel of its successors, legal representatives and assigns, shall advise that any proceeding in connection with said inventions, or said application for Letters Patent, or any proceeding in connection with Letters Patent for said inventions in any country, including interference proceedings, is lawful and desirable, or that any division, continuation or continuation-in-part of any application for Letters Patent or any reissue or extension of any Letters Patent, to be obtained thereon, is lawful and desirable, sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done for the procurement, maintenance, enforcement and defense of Letters Patent for said inventions, without charge to said assignee Cadence Design Systems, Inc., its successors, legal representatives and assigns, but at the cost and expense of said assignee Cadence Design Systems, Inc., its successors, legal representatives and assigns.

AND said assignors hereby request the Commissioner of Patents to issue said Letters Patent of the United States to said assignee Cadence Design Systems, Inc. as the assignee Cadence Design Systems, Inc. of said inventions and the Letters Patent to be issued thereon for the sole use of said assignee Cadence Design Systems, Inc., its successors, legal representatives and assigns.

5/19/04
Date

Igor KELLER

5/19/04
Date

Kenneth TSENG

5/19/04
Date

Nishath VERGHESE

SF-1656920